



राष्ट्रीय प्रौद्योगिकी संस्थान गोवा
NATIONAL INSTITUTE OF TECHNOLOGY GOA
Farmagudi, Ponda, Goa - 403 401, India
Department of Electrical and Electronics Engineering

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Provisionally Shortlisted JRF candidates for Interview

The provisionally shortlisted candidates for the interview of Junior Research Fellow position is attached herewith w.r.t Ref. No. **NITG/ADMIN/223/OW/461** dt. 19/10/2023 under the project Chips to Startup (C2S) Programme, MeiTY, Govt. of India project titled “ Design and Development of FPGA Accelerator IP for Deep Neural Network (FAipDNet)” sponsored by Ministry of Electronics and Information Technology (MeiTY), Government of India.

The list of shortlisted candidates for interview is given below:

Sr. No.	Name
1.	PREMANAND KISANRAO KADBE
2.	BHUKY THIRUPATHI
3.	CHINMAY DHOLE
4.	PERIKA PUNEETH

Reporting Time : 09:00 AM on 23rd November, 2023 (Thursday)
Venue : Simulation Lab, Dept of EEE, NIT GOA
Date of Interview : 23rd November, 2023 (Thursday) 10.00 A.M. to 01.30 P.M.

Note:

- JRF will be appointed initially for one year (on contract) and his/her services will be extended for subsequent year(s) based on the performance review. The position is co-terminus with the project.
- Please confirm your attendance by e-mail on or before 20th November, 2023.
- Please bring your certificates including GATE score card along with all supporting documents. Also bring a 'No Objection Certificate' if you are already employed in a Government Organization, failing which your candidature will not be considered.
- The selection process is expected to get completed by the end of the day.

Sd/-

Dr. Amol D. Rahulkar
PI of the Project