



NATIONAL INSTITUTE OF TECHNOLOGY GOA

(An Institution of National Importance under MHRD, Govt. of India)

Department of Electrical and Electronics Engineering

Shortlisted candidates for the post of JRF

Ref No. (NITG/EEE/ADVT/2018/01 dated 16/03/18)

With reference to the application for the post of JRF for the SERB sponsored project, “Design and Implementation of an Inverter for a Grid Connected Photovoltaic System which is part of a virtual power plant”, the following candidates are requested to appear for written test followed by an interview:

SI No	Name	SI No	Name
1	Ajay Panday	18	Kotagiri Naveen
2	Ajit Dattu Nandawadekar	19	Krishnaprasad. K.
3	Aneesh S	20	L Harshavardhan
4	Anilkumar Alamanda	21	Nihal Kawale
5	Arunkumar C R	22	Pooja Krishna M R
6	Bodireddy Janardhan	23	Ravi Ranjan
7	Boodida Keerthi Kumar	24	Renu O V
8	Chintalapudi L Narayana	25	Rohan Raghunath Kedare
9	Devakumar Annavaram	26	Shivaji Manik Awchar
10	Dogga Ramchandra Rao	27	Swikruti Suprava
11	Duttatreya Roy Chowdhury	28	Syed Minalla
12	G Krishna Reddy	29	Ujjval B Vyas
13	G Sarat Prabhu	30	Vaishali chapparya
14	Gayathri teja	31	Vidya V
15	Hitha K Prabhakaran	32	Vivek Kumar Tripathi
16	Kaushal Prakash	33	Yashoda Halyal
17	Kaushik Kushalappa Naik		

Reporting time

Date May 15, 2018
Time 9:00 AM
Venue NIT Goa, Conference Room, Main Building

Note:

1. Please confirm your attendance by e-mail.
2. Please bring your original certificates along with all supporting documents. Also bring a 'No Objection Certificate' if you are already employed in a Government Organisation, failing which your candidature will not be considered.



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3. The details provided by the applicant have not been verified in this stage. The candidate may not be selected if any of the conditions specified in the advertisement is not met.
4. The selection process is expected to get completed by the end of the day.

ADDRESS FOR CORRESPONDENCE

Dr. Sreeraj E S,

Assistant Professor, Dept of EEE,

National Institute of Technology Goa,

Inside GEC Campus, Near Govt. ITI,

Farmagudi, Ponda, 403401

Goa- India,

Phone: 0832 - 2404218

E Mail : sreeraj@nitgoa.ac.in