



National Institute of Technology Goa

(An Institution of National Importance under MHRD, Govt. of India)

Department of Electronics and Communication Engineering

Advt. No.: NITG/ECE/2019/M/1059

Dated: 29/05/2019

Advertisement for the Position of JRF

Applications are invited from the interested candidates for the post of JRF to work on the R&D project titled "Design and Development of Compact and Wideband Dielectric Resonator Antennas" sanctioned by Science and Engineering Research Board (SERB), New Delhi. The staff will be appointed initially for one year (on contract) and their services will be extended for next year based on the performance review.

S No.	Position	Area of Specialization	Maximum Duration	Consolidated Salary	Number of Positions
1	JRF- Junior Research Fellow	B.Tech./B.E. in Electronics and Communications Engineering /M.Tech./M.E./MS(R) in RF & Microwave Engineering/ Communications Engineering with strong knowledge in antenna design using EM tool such as CST/HFSS	27 months	Rs. 25,000/- (Per month)	One (01)

Eligibility:

1. B.Tech./B.E with valid GATE score or M.Tech./M.E./MS(R)/equivalent in RF & Microwave Engineering/Communications Engineering with expertise in antenna design using CST Microwave Studio or other EM simulation software. Candidates must have at least 6.5 CGPA or 60% marks in aggregate from a recognized technical institute or university as a full time program.

Objective of the Project

The main objective of this project is to design and develop compact and wideband dielectric resonator antennas.

Application Process

Application form (as given below) giving all the details and attested copies of certificates, supporting documents and experience should reach the undersigned latest by **June 25, 2019**. Candidates who are already employed should produce relieving certificate from their employers, if selected. The written test/interview will be conducted for all the eligible candidates. The applicants need to bring all the original documents for verification during written test/interview. The list of shortlisted candidates will be posted in the NIT Goa website (www.nitgoa.ac.in) along with the date and time of the written test/interview.



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Attention:

1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
3. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
4. Shortlisted Candidates will be informed by email (apart from website) about the written test/interview date. So, the Candidate must provide valid E-mail IDs in their applications.
5. Shortlisted candidates have to present themselves for the written test/interview on the written test/interview date with updated CV and original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
6. Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.
7. No TA/DA will be paid for appearing in the interview.
8. Bio-data format is being provided with this notification. The candidates are required to bring duly filled in bio-data format along with photocopies of required documents.
9. Last date for receiving the duly filled in application is **June 25, 2019**.
10. Intimation regarding written test/interview shall be posted on the Institute website.

Address for Correspondence:

To,
Dr. Pragati Patel,
Assistant Professor, Department of Electronics and Communication Engineering,
National Institute of Technology Goa,
Farmagudi, Ponda, 403401
Goa- India,

Telephone: 0832-2404204
Email: pragati@nitgoa.ac.in
Website: www.nitgoa.ac.in

NOTE: The envelope containing the application should be super scribed as “**Application for the position of JRF under SERB project on Design and Development of Compact and Wideband Dielectric Resonator Antennas**”.



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Application for the Position of JRF under SERB

1. Post Applied for : JRF-Junior Research Fellow

2. Name of the Candidate (BLOCK LETTER):

3. Father's Name (BLOCK LETTER):

4. Mother's Name (BLOCK LETTER):

5. (a) Date of Birth: (DD/MM/YYYY)_____

(b) Sex (Male/Female/Other):__ _

(c) Marital Status: Married/Single

(d) Category: SC/ST/OBC/PWD/Open

6. Previous Research experience: (use additional sheet if required)_____

7. Publication(s), if any: (use additional sheet if required)_____

8. GATE/ NET (if any): Qualified (Yes/No): Score: _____ Rank: _____
Specialization: _____ Year: _____

9. Academic Qualification: (Starting from 10th Standard or equivalent Examination)

Name of Exam Passed	Name of the School/College/Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/ CGPA

Paste here a recent Passport size color Photograph



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10. (a) Address for Communication: (BLOCK LETTERS):

(b) Contact No (Mob.):
(c) E-mail ID :

11. Contact Details of two referees:

	Referee I	Referee II
Name :		
Designation :		
Organization:		
Office Address :		
Office Phone Number:		
Email ID:		

12. Experience in antenna design using CST Microwave Studio/HFSS if any (in years and use additional sheet if required):

Declaration

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:
Place:

Signature

Note- If any extra information needs to be furnished, extra A4 sheets may be attached.