



# NATIONAL INSTITUTE OF TECHNOLOGY GOA

(An Institution of National Importance under MHRD, Govt. of India)

Department of Electrical and Electronics Engineering

## **Shortlisted candidates for the post of JRF**

Ref No. (NITG/EEE/2019/OW/152 dated 5th April, 2019)

With reference to the application for the post of JRF for the SERB sponsored project, “Design and Implementation of an Inverter for a Grid Connected Photovoltaic System which is part of a virtual power plant,” the following candidates are requested to appear for written test followed by an interview:

SI No	Name
1	V VEERAMANI
2	SUBHRANSHU SEKHAR PATI
3	Yashoda Halyal
4	Abhishek Junghare
5	Sthrigdhara Naik
6	RUPALI BISEN
7	Uma Maheswari
8	Kavitha Suresh
9	Radhika Vaze
10	Suresh Reddy Mekapati
11	Lohith HB
12	Abhay Singh
13	Puneet Chandel

### **Reporting time**

Date 25th May, 2019, Saturday  
Time 10:00 AM  
Venue National Institute of Technology Goa

### **Note:**

1. Please confirm your attendance by e-mail.
2. Please bring your original certificates along with all supporting documents. Also bring a 'No Objection Certificate' if you are already employed in a Government Organisation, failing which your candidature will not be considered.
3. The details provided by the applicant have not been verified in this stage. The candidate may not be selected if any of the conditions specified in the advertisement is not met.
4. The selection process is expected to get completed by the end of the day.



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## **ADDRESS FOR CORRESPONDENCE**

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