

NATIONAL INSTITUTE OF TECHNOLOGY – GOA

Time Table for the Odd Semester August 2018 – December 2018 (with effect from 01/08/2018)

Department of Electronics and Communication Engineering

Sem : III

Lecture Hall: 1A

Day/ Time	09.00 – 9.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50– 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
Monday	EMT	S&S Lab			L U N C H			
Tuesday	AE	EMT	S&S	NTS				
Wednesday	MAT	S&S	AE	EMT		AE Lab		
Thursday	S&S	MAT	EMT	NTS				
Friday		NTS	S&S	AE		NTS	MAT	

AE - EC201 - Analog Electronics - Dr. E. Mallikarjun

S&S - EC202 - Signals & Systems – Dr. Shivnarayan Patidar

NTS - EC203 - Network Theory & Synthesis – Dr. T.Veerakumar

EMT - EC204 - Electromagnetic Theory – Dr. Anirban Chatterjee

MAT - MA200 - Mathematics – III - Dr. Ravi Ragoju & Dr. L. Shangerganesh

AE Lab - EC205 - Analog Electronics Laboratory - Dr. Shivanarayan Patidar & Dr. Anirban Chatterjee

S&S Lab - EC206 - Signals & Systems Laboratory - Dr. T.Veerakumar & Dr. Trilochan Panigrahi

Prepared by

HOD ECE

DIRECTOR

NATIONAL INSTITUTE OF TECHNOLOGY – GOA

Time Table for the Odd Semester August 2018 – December 2018 (with effect from 01/08/2018)

Department of Electronics and Communication Engineering

Sem : V

Lecture Hall: 2B

Day/ Time	09.00 – 9.50	10.00 – 10.50	11.05 –11.55	12.00 – 12.50	12.50– 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
Monday	DSA	ES	MPMC	DSP				
Tuesday	ES	DSP Lab				DSA	DSP	CS
Wednesday		MPMC	DSP	CS				
Thursday	MPMC	DSP	DSA			CS	ES	CS
Friday	DSA	MPMC Lab						

ES - ES300 – Environmental Studies- Dr. Velavan K.

CS - EC302 – Control System – Dr. Ankit Dubey & Pragati Patel

DSP - EC303 – Digital Signal Processing – Dr. Lalit K. Jiwani

MPMC- EC304 – Microprocessor and Microcontroller – Dr. Prashanth GR

DSP Lab - EC305 - Digital Signal Processing Laboratory - Dr. Lalit K. Jiwani

MPMC Lab - EC306 – Microprocessor and Microcontroller Laboratory – Dr. Prashanth GR

DSA – EC301 – Data Structures and Algorithms – Dr. S. Mini

Prepared by

HOD ECE

DIRECTOR

NATIONAL INSTITUTE OF TECHNOLOGY – GOA

Time Table for the Odd Semester August 2018 – December 2018 (with effect from 01/08/2018)

Department of Electronics and Communication Engineering

Sem : VII

Lecture Hall: N1

Day/ Time	09.00 – 9.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50– 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
Monday		ITC	MAN(N8)		L U N C H	Major Project		
Tuesday	DIP	VCD	ITC					
Wednesday	ITC	MAN(N8)		DIP		VD Lab		
Thursday	MAN(N8)	DIP	VCD					
Friday	VCD	Seminar					Major Project	

VCD - EC401- VLSI Circuit Design – Dr. Vasantha MH & Dr. Nithin Kumar YB

MAN – HS400 – Management - Dr. Sunil Kumar

ITC – EC402 – Information and Theory Coding – Dr. Ankit Dubey

DIP - EC421 - Digital Image Processing – Dr.T.Veerakumar

VD Lab – EC403 – VLSI Design Laboratory – Dr. Vasantha MH & Dr. Nithin Kumar YB

Sem - EC448 - Seminar – Dr.Anirban Chatterjee

EC449 - Major Project – Dr. Trilochan Panigrahi

Prepared by

HOD ECE

DIRECTOR

NATIONAL INSTITUTE OF TECHNOLOGY – GOA

Time Table for the Odd Semester August 2018 – December 2018 (with effect from 01/08/2018)

Department of Electronics and Communication Engineering

M.Tech : VLSI

Sem : I

Lecture Hall: N5

Day/ Time	09.00 – 9.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50– 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
Monday		AIC	DIC	SDT	L U N C H	Seminar		
Tuesday	AIC	DSP		OT		ICD Lab		
Wednesday	SDT	DIC	AIC	DSP				
Thursday	DIC	SDT	OT			ICD Lab		
Friday	OT	DSP				SDS Lab		

DIC - EC600 - Digital IC Design – Dr. Vasantha MH

AIC - EC601 - Analog IC Design – Dr. Nithin Kumar YB

SDT - EC602 - Semiconductor Device Theory and Modelling – Dr. Lalat Indu Giri

DSP - EC603 - Digital Signal Processing – Dr. Trilochan Panigrahi

OT – EC852 - Optimization Techniques – Dr. Ravi Prasad KJ

ICD Lab - EC604 - IC Design Laboratory - Dr. Vasantha MH & Dr. Nithin Kumar YB

SDS Lab - EC605 - Semiconductor Device Simulation Laboratory – Dr. Lalat Indu Giri

EC606 – Seminar - Dr. Vasantha MH & Dr. Prashanth GR

Prepared by

HOD ECE

DIRECTOR