

# NATIONAL INSTITUTE OF TECHNOLOGY – GOA

Time Table for the Odd Semester August 2017 – December 2017 (with effect from 2/08/2017)

## Department of Electronics and Communication Engineering

**Sem : III**

**Lecture Hall: 1A**

Day/ Time	09.00 – 09.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50 – 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
<b>Monday</b>		<b>SS</b>	<b>AE</b>	<b>MAT</b>	<b>L U N C H</b>	<b>SS Lab</b>		
<b>Tuesday</b>	<b>SS</b>		<b>EMT</b>	<b>NTS</b>		<b>AE</b>		<b>NTS(T)</b>
<b>Wednesday</b>	<b>MAT</b>	<b>EMT</b>	<b>SS</b>					
<b>Thursday</b>	<b>AE</b>	<b>MAT</b>	<b>SS</b>	<b>NTS</b>		<b>EMT</b>		
<b>Friday</b>		<b>EMT</b>		<b>NTS</b>		<b>AE Lab</b>		

**AE - EC201 - Analog Electronics - Dr. Shivnarayan Patidar- Conference Hall/ITI Building (CH/ITI)**

**SS - EC202 - Signals and Systems - Dr. Badri Narayan Subudhi**

**NTS - EC203 - Network Theory and Synthesis - Dr. T. Veerakumar**

**EMT - EC204 - Electromagnetic Theory - Dr. Anirban Chatterjee**

**MAT - MA200 - Mathematics –III – Dr. Raviprasad K J**

**AE Lab -EC205 - Analog Electronics Laboratory - Dr. Shivnarayan Patidar & Dr. E Mallikarjun**

**SS Lab - EC206 - Signals and Systems Laboratory – Dr. Badri Narayan Subudhi & Dr. T Veerakumar**

# NATIONAL INSTITUTE OF TECHNOLOGY – GOA

Time Table for the Odd Semester August 2017 – December 2017 (with effect from 2/08/2017)

## Department of Electronics and Communication Engineering

**Sem : V**

**Lecture Hall: 2B, CH/ITI, N8**

Day/ Time	09.00 – 09.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50 – 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
<b>Monday</b>	MPMC	DSP	DSA	CS	<b>L U N C H</b>		ES (N8)	
<b>Tuesday</b>	DSP Lab					DSA (T)	DSP (T)	
<b>Wednesday</b>	CS	MPMC	DSP	DSA			ES (N8)	
<b>Thursday</b>	DSA	CS				MPMC Lab - EC306		
<b>Friday</b>	MPMC(N8)	ES (N8)	DSP			CS		

**ES - ES300 - Environmental Studies – Dr. Velavan Kathirvelu**

**DSA - EC301 - Data Structures and Algorithm – Dr. S. Mini**

**CS - EC302 - Control System - Dr. Pragati Patel**

**DSP - EC303 - Digital Signal Processing - Dr. Lalit K Jiwani**

**MPMC - EC304 - Microprocessor and Microcontroller - Dr. Prasanth G R-Conference Hall/ITI Building (CH/ITI)**

**DSP Lab -EC305 - Digital Signal Processing Laboratory - Dr. Lalit K Jiwani & Dr. Trilochan Panigrahi**

**MPMC Lab - EC306 - Microprocessor and Microcontroller Laboratory - Dr. Prasanth G R & Dr. Shivnarayan Patidar**

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Time Table for the Odd Semester August 2017 – December 2017 (with effect from 2/08/2017)

## Department of Electronics and Communication Engineering

**Sem : VII**

**Lecture Hall: N2**

Day/ Time	09.00 – 09.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50 – 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
<b>Monday</b>		<b>VLSI</b>	<b>MGT</b>	<b>IP</b>	<b>L U N C H</b>	<b>Major Project</b>		
<b>Tuesday</b>	<b>VLSI</b>	<b>RNS</b>		<b>ITC</b>		<b>B. Tech Industrial Training</b>		
<b>Wednesday</b>	<b>IP</b>	<b>MGT</b>	<b>VLSI</b>	<b>RNS</b>		<b>Major Project</b>		
<b>Thursday</b>	<b>MGT</b>	<b>IP</b>		<b>ITC</b>		<b>VLSI Lab</b>		
<b>Friday</b>		<b>ITC</b>	<b>RNS</b>			<b>Seminar</b>		

**VLSI - EC401 - VLSI Circuit Design - Dr. Vasantha M H and Dr. Nithin Kumar Y B**

**MGT - HS400 – Management - Dr. Sunil Kumar- Conference Hall/ITI Building (CH/ITI)**

**ITC - EC402 - Information Theory and Coding - Dr. Ankit Dubey**

**IP - EC421 - Image Processing –Dr. T. Veerakumar**

**RNS - EC420 - Radar and Navigation Systems – Dr. E. Mallikarjun**

**VLSI Lab - EC403 - VLSI Design Laboratory - Dr. Vasantha M H and Dr. Nithin Kumar Y B**

**EC448 - Seminar – Dr. Lalit K Jiwani & Dr. Badri N Subudhi**

**EC449 - Major Project - Dr. Trilochan Panigrahi**

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## Department of Electronics and Communication Engineering

**Sem : M.TECH VLSI (I)**

**Lecture Hall: N5**

Day/ Time	09.00 – 09.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50 – 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
<b>Monday</b>			<b>DSP</b>	<b>DIC</b>	<b>L U N C H</b>	<b>ICD Lab</b>		
<b>Tuesday</b>			<b>AIC</b>	<b>SDMT</b>		<b>SDS Lab</b>		
<b>Wednesday</b>	<b>DIC</b>	<b>DSP</b>				<b>ICD Lab</b>		
<b>Thursday</b>	<b>DSP</b>	<b>DIC</b>	<b>SDMT</b>	<b>AIC</b>				
<b>Friday</b>	<b>SDMT</b>	<b>AIC</b>				<b>Seminar</b>		

**DIC - EC600 - Digital IC Design - Dr. Vasantha M H**

**AIC - EC601 - Analog IC Design - Dr. Nithin Kumar Y B**

**SDMT - EC602 - Semiconductor Device Theory and Modelling - Dr. Lalat Indu Giri**

**DSP - EC603 - Digital Signal Processing - Dr. Trilochan Panigrahi**

**ICD Lab - EC604 - IC Design Laboratory - Dr. Nithin Kumar Y B, and Dr. Anirban Chaterjee**

**SDS Lab - EC605 - Semiconductor Device Simulation Laboratory - Dr. Lalat Indu Giri**

**EC606 – Seminar - Dr. Nithin Kumar Y B and Dr. Vasantha M H**

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Time Table for the Odd Semester August 2017 – December 2017 (with effect from 2/08/2017)

## Department of Electronics and Communication Engineering

Sem : M.TECH VLSI (III)

Lecture Hall: N5

Day/ Time	09.00 – 09.50	10.00 – 10.50	11.05 – 11.55	12.00 – 12.50	12.50 – 02.00	02.00 – 02.50	03.00 – 03.50	04.00 – 04.50
Monday		RFIC			L U N C H	MProj		
Tuesday						MProj		
Wednesday				RFIC		MProj		
Thursday							OT	OT
Friday				RFIC				OT

RFIC - EC815 - CMOS RF IC Design-Dr. Anirban Chaterjee

OT - EC 852 - Optimization Techniques-Dr. Ravi Prasad K J

MProj-EC700 - Major Project-Dr. Nithin Kumar Y B